

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of: Ryosuke KOSAKA

Appl. No.:

Group:

Filed:

February 4, 2002

Examiner:

For: SIGNAL LINE DRIVING CIRCUIT AND SIGNAL
LINE DRIVING METHOD FOR LIQUID CRYSTAL DISPLAY

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, DC 20231

February 4, 2002

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE SPECIFICATION:

Please replace the paragraph beginning on page 12, line 25, with the following rewritten paragraph:

-- If the compared result indicates a disagreement, namely, if the comparison result signal $CMP = H$, the switch controller 53 turns on the analog switch SW2, and pre-charges the signal line Sn to the pre-set middle potential Vp. In succession, it turns off the analog switch SW2, and simultaneously turns on the analog switch SW2, and thereby applies the gradation voltage outputted from the output amplifier

REMARKS

Entry of the above amendments is earnestly solicited. An early and favorable first action on the merits is earnestly requested.

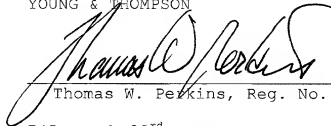
Should there be any matters that need to be resolved in the present application, the Examiner is respectfully requested to contact the undersigned at the telephone number listed below.

Attached hereto is a marked-up version of the changes made to the specification by the current amendment. The attached page is captioned "VERSION WITH MARKINGS TO SHOW CHANGES MADE."

The Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 25-0120 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17.

Respectfully submitted,

YOUNG & THOMPSON



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TWP/ia
Attachments

VERSION WITH MARKINGS TO SHOW CHANGES MADEIN THE SPECIFICATION:

The paragraph beginning on page 12, line 25, has been amended as follows:

If the compared result indicates a disagreement, namely, if the comparison result signal $CMP = H$, the switch controller 53 turns on the analog switch SW2, and pre-charges the signal line S_n to the pre-set middle potential V_p . In succession, it turns off the analog switch SW2, and simultaneously turns on the analog switch SW2, and thereby applies the gradation voltage outputted from the output amplifier 463 to the signal line S_n . The switching operation between the analog switches SW1 and SW2 is done at a pre-set timing (T_1) in accordance with the signal SP outputted from the external timing generator.